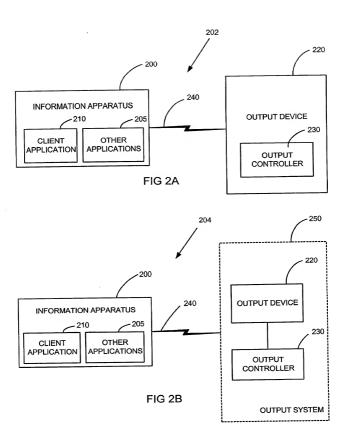
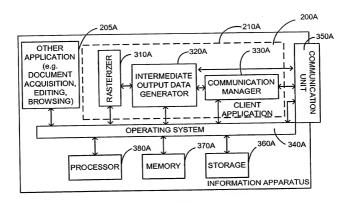


FIG 1 A

FIG 1 B





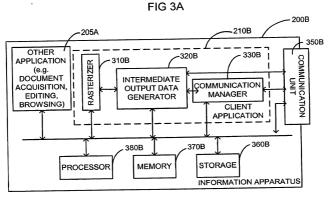
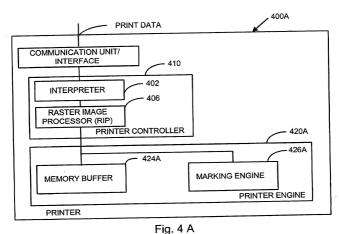


FIG 3B



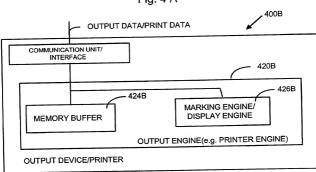


Fig. 4 B

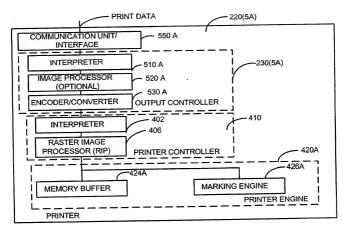


Fig. 5 A

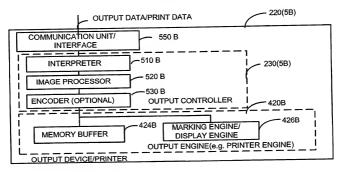
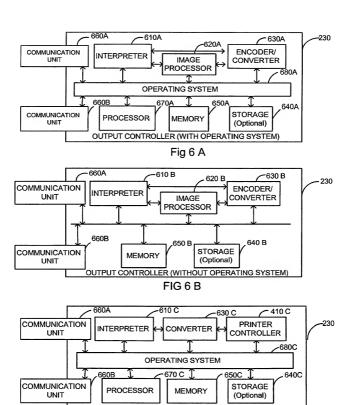


Fig. 5 B



OUTPUT CONTROLLER (COMBINED WITH PRINTER CONTROLLER)

FIG 6 C

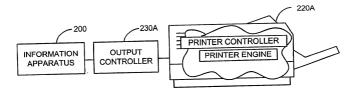


Fig. 7A

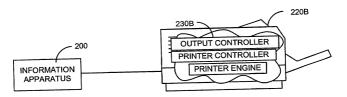


Fig. 7B

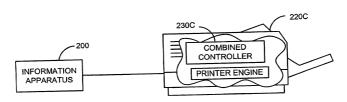


Fig. 7C

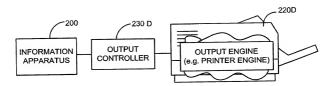


FIG 7D

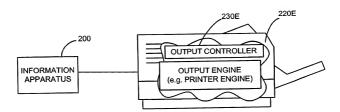


FIG 7E

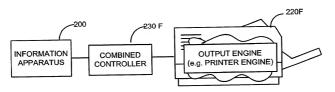


FIG 7F

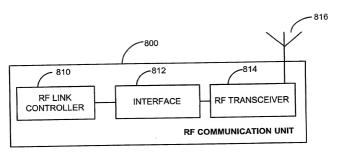


FIG 8A

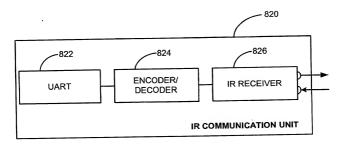


FIG 8B

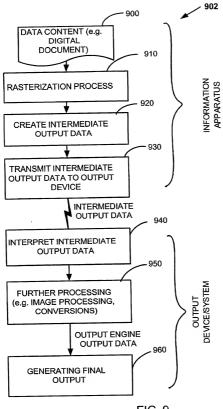
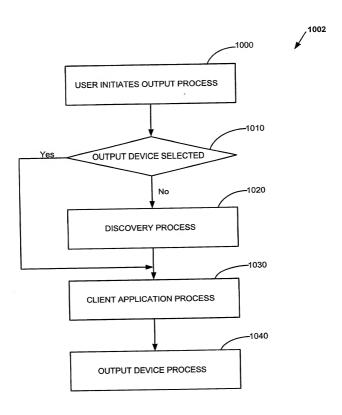
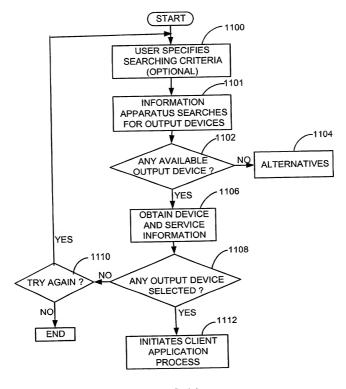


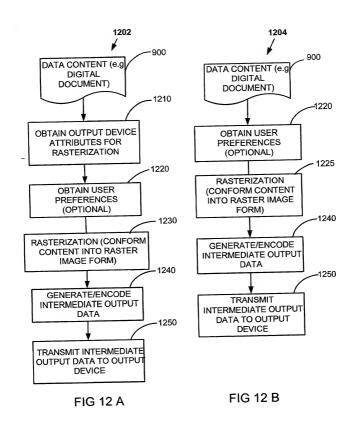
FIG 9



**FIG 10** 



**FIG 11** 



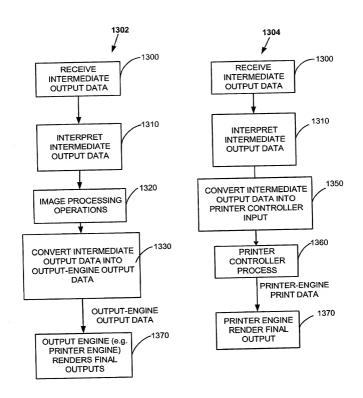


FIG 13 A

FIG 13 B